

11-26-90

M. Cole



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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

JOSEPH T. EVANS, JR. ET AL

For:

NON-VOLATILE MEMORY CIRCUIT USING  
FERROELECTRIC CAPACITOR STORAGE ELEMENT

## PRIOR APPLICATION:

Serial No: 440,434  
 Filed: 11/20/89  
 Group: 233

"Express Mail" mailing label  
 number RB156288781

Date of Deposit September 14, 1990

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231.

M. Walke

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*M. Walke*

(Signature of person mailing paper or fee)

Honorable Commissioner of  
 Patents and Trademarks  
 Washington, D.C. 20231

Dear Sir:

PRELIMINARY AMENDMENT

Prior to initial review, please amend as follows the accompanying Rule 1.60 continuation application of pending prior Application Serial No. 440,434, filed on November 20, 1989.

IN THE SPECIFICATION

Page 7, line 8, delete "and".

Page 7, line 12, delete "." and substitute --, and --.

Page 7, line 13, add --Figure 17 is an alternate

B' configuration for the circuit shown in Figure 2--.

Page 10, line 8, after "reversed." add --An alternate  
 circuit configuration 20' illustrating this reversal is shown in  
 Figure 17. The circuit in Figure 17 is essentially the same as  
 that shown in Figure 2, but with the bit and drive lines  
 interchanged and the word line made perpendicular--.

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